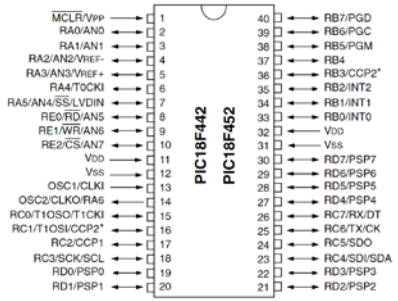
High-Level View of a PIC18 Microcontroller



Program ROM

Stack

PC: Program Counter

Data RAM

"File Registers"

SFRs

EEPROM

Program Bus

CPU

Data Bus

Interrupt Control Logic

Oscillator (Clock)

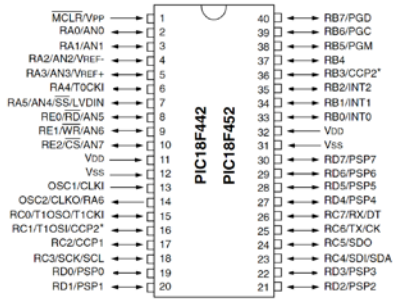
Digital I/O Ports

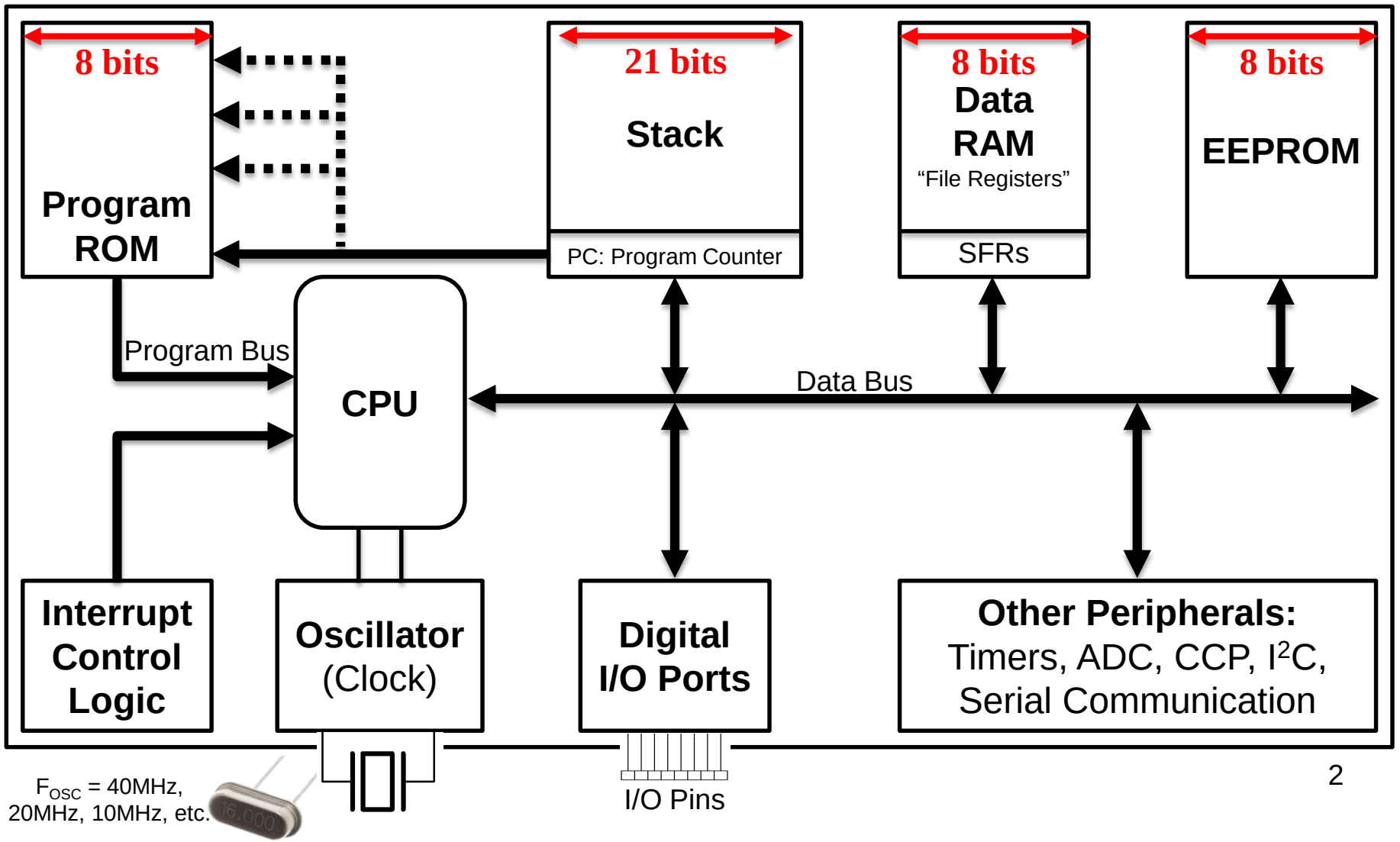
Other Peripherals: Timers, ADC, CCP, I^2C, Serial Communication

F_{OSC} = 40MHz, 20MHz, 10MHz, etc.

I/O Pins

High-Level View of a PIC18 Microcontroller





ROM & RAM Sizes

→ PIC18F452 Actual Sizes

→ PIC18 Family MAX Sizes